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QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09/838,520</u>	Prepared by <u>MPB</u>	Tracking Number <u>05891235</u>	
Examiner-GAU <u>DeBerardinis - 2836</u>	Date <u>3/22/04</u>	Week Date <u>1/19/04</u>	
	No. of queries <u>2</u>	<u>1FW</u>	

JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<u>q. PTO-1449</u>
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	MESSAGE
a. Page Missing	<p>① <u>PTO-1449: Please either initial or line through citations. (copy provided for reference).</u></p> <p>② <u>Claims 2, 5, 6, 7 and 11 (originally claims 16, 19, 20, 21 and 22, respectively), all depends on claim 14 (originally claim 15). — see attached.</u></p> <p><u>Please advise/correct claim dependency.</u></p> <p><i>Thunlazer</i></p>
b. Text Continuity	
c. Holes through Data	
d. Other Missing Text	
e. Illegible Text	
f. Duplicate Text	
g. Brief Description	
h. Sequence Listing	
i. Appendix	
j. Amendments	
k. Other	
<b>CLAIMS</b>	
a. Claim(s) Missing	
<u>b. Improper Dependency</u>	
c. Duplicate Numbers	
d. Incorrect Numbering	
e. Index Disagrees	
f. Punctuation	
g. Amendments	
h. Bracketing	
i. Missing Text	
j. Duplicate Text	
k. Other	
	initials <i>OMH</i>
	<b>RESPONSE</b>
	initials

FORM PTO-1449/A and B (Modified)				APPLICATION NO.: 09/838,520		ATTY. DOCKET NO.: A00312.70393			
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				FILING DATE: April 19, 2001		CONFIRMATION NO.: 9453			
				APPLICANT: Geoffrey T. Haigh et al					
				GROUP ART UNIT: 2836		EXAMINER: R.L. Deberadinis			
Sheet	1	of	1						

#### U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		

#### FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			
		DE	19718420A1		Siemens AG	11-12-98	Y
		DE	19922123A1		Siemens AG	11-23-00	Y
		DE	19922127A1		Siemens AG	11-23-00	Y
		DE	19922127C2		Siemens AG	05-29-02	Y
		DE	19922128C1		Siemens AG	01-25-01	Y
		DE	19922129C1		Siemens AG	09-28-00	Y

#### OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_, filed \_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]

In the Claims

Applicant has submitted a complete new claim set showing marked up claims with insertions indicated by underlining.

Amend claims 14 and 15 as follows:

Claims 1-13 (cancelled)

14. (currently amended) An information signal isolator comprising:  
a first substrate;  
a first passive component formed on the first substrate;  
an isolation layer formed over the first passive component;  
a second passive component formed over the isolation layer;  
the first and second passive components being coils;  
an input for receiving an input signal; and  
a driver circuit coupled between the input and one of said passive components.

15. (currently amended) An information isolator comprising:  
a first substrate;  
a first passive component formed on the first substrate;  
an isolation layer formed over the first passive component;  
a second passive component formed over the isolation layer;  
the first and second passive components being capacitor plates;  
an input for receiving an input signal; and  
a driver circuit coupled between the input and one of said passive components.

16. (previously presented) The isolator of claim 14 or claim 15 wherein the first substrate is a semiconductor substrate.

<sup>3</sup>17. (previously presented) The isolator of claim <sup>2</sup>16, wherein the driver circuit also is formed on the first semiconductor substrate.

<sup>4</sup>18. (previously presented) The isolator of claim <sup>2</sup>16, further comprising a second substrate, wherein the driver circuit is formed on the second substrate.

<sup>15</sup>19. (previously presented) The isolator of claim <sup>1</sup>14 or claim <sup>14</sup>15 wherein the first passive component is formed on top of the first substrate.

<sup>6</sup>20. (previously presented) The isolator of claim <sup>1</sup>14 or claim <sup>14</sup>15 wherein the first passive component is formed into the first substrate.

<sup>7</sup>21. (previously presented) The isolator of claim <sup>1</sup>14 or claim <sup>14</sup>15, further comprising a third passive component on the substrate, a second isolation layer over the third passive component, and a fourth passive component formed over the second isolation layer, wherein the driver circuit provides signals to the first and third passive components.

<sup>11</sup>22. (previously presented) The isolator of claim <sup>1</sup>14 or claim <sup>14</sup>15, further comprising a third passive component on the substrate, a second isolation layer over the third passive component, and a fourth passive component formed over the second isolation layer, wherein the driver circuit provides signals to the second and fourth passive components.

<sup>12</sup>23. (previously presented) The isolator of claim <sup>11</sup>22 wherein the first and second isolation layers are a single layer.

<sup>13</sup>24. (previously presented) The isolator of claim <sup>1</sup>14 wherein the first and second passive components are referenced to separate, galvanically isolated grounds, and further including a Faraday shield between the first and second passive components,